

SIMATIC S7-400H, CPU 417H CENTRAL UNIT FOR S7-400H 4
 INTERFACES: 1 MPI/DP, 1 DP A. 2 FOR SYNC MODULES 20MB
 MEMORY (10MB CODE/10MB DATA)



General information	
Product type designation	CPU 417H
Supply voltage	
Rated value (DC)	Yes
<ul style="list-style-type: none"> • 24 V DC 	
Input current	
from backplane bus 5 V DC, max.	1.7 A
Power loss	
Power loss, typ.	6 W
Memory	
Work memory	
<ul style="list-style-type: none"> • integrated 	20 Mbyte
<ul style="list-style-type: none"> • expandable 	No
Load memory	
<ul style="list-style-type: none"> • expandable FEPR0M 	Yes; with Memory Card (FLASH)
<ul style="list-style-type: none"> • expandable FEPR0M, max. 	64 Mbyte
<ul style="list-style-type: none"> • integrated RAM, max. 	256 kbyte

• expandable RAM	Yes; with Memory Card (RAM)
• expandable RAM, max.	64 Mbyte
Backup	
• present	Yes
• with battery	Yes; all data
• without battery	No
Battery	
Backup battery	
• Backup current, typ.	600 μ A
• Backup current, max.	1 810 μ A
• Feeding of external backup voltage to CPU	5 V DC to 15 V DC
CPU processing times	
for bit operations, typ.	0.03 μ s
for word operations, typ.	0.03 μ s
for fixed point arithmetic, typ.	0.03 μ s
for floating point arithmetic, typ.	0.09 μ s
CPU-blocks	
DB	
• Number, max.	8 192; DB 0 reserved
• Size, max.	64 kbyte
FB	
• Number, max.	6 144
• Size, max.	64 kbyte
FC	
• Number, max.	6 144
• Size, max.	64 kbyte
OB	
• Number, max.	see instruction list
• Size, max.	64 kbyte
Nesting depth	
• per priority class	24
• additional within an error OB	2
Counters, timers and their retentivity	
S7 counter	
• Number	2 048
Retentivity	
— adjustable	Yes
— lower limit	0
— upper limit	2 047
— preset	Z 0 to Z 7

Counting range	
— lower limit	1
— upper limit	999
S7 times	
• Number	2 048
Time range	
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	
• present	Yes
• Type	SFB
Data areas and their retentivity	
Flag	
• Number, max.	16 kbyte
• Retentivity available	Yes; MB 0 to MB 16383
• Retentivity preset	MB 0 to MB 15
Address area	
I/O address area	
• Inputs	16 kbyte
• Outputs	16 kbyte
of which distributed	
— MPI/DP interface, inputs	2 kbyte
— MPI/DP interface, outputs	2 kbyte
— DP interface, inputs	8 kbyte
— DP interface, outputs	8 kbyte
Process image	
• Inputs, adjustable	16 kbyte
• Outputs, adjustable	16 kbyte
• Inputs, default	1 024 byte
• Outputs, default	1 024 byte
Subprocess images	
• Number of subprocess images, max.	8
Digital channels	
• Inputs	131 072
— of which central	131 072
• Outputs	131 072
— of which central	131 072
Analog channels	
• Inputs	8 192
— of which central	8 192
• Outputs	8 192